

N-Channel Enhancement Mode Power MOSFET

DESCRIPTION

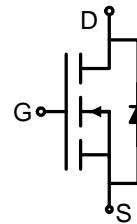
The PE02N05A uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

GENERAL FEATURES

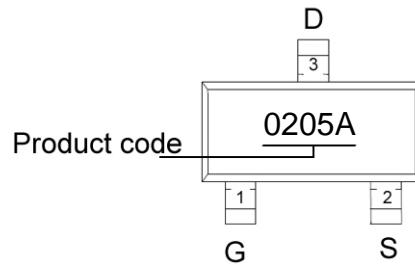
- $V_{DS} = 60V, I_D = 2.8A$
- $R_{DS(ON)} < 100m\Omega @ V_{GS}=10V$
- $R_{DS(ON)} < 115m\Omega @ V_{GS}=4.5V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

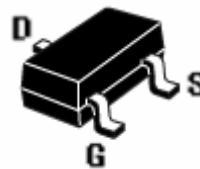
- Battery Switch
- DC/DC Converter



Schematic diagram



Marking and pin Assignment



SOT-23 top view

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	2.8	A
Drain Current-Pulsed (Note 1)	I_{DM}	10	A
Maximum Power Dissipation	P_D	1.7	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	73.5	°C/W
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Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$	-	-	1	μA

Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	1.0	1.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=2.8A$	-	87	100	$m\Omega$
		$V_{GS}=4.5V, I_D=2.8A$	-	95	115	$m\Omega$
		$V_{GS}=2.5V, I_D=2A$	-	130	150	$m\Omega$
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$V_{DS}=30V, V_{GS}=0V, F=1.0MHz$	-	247	-	PF
Output Capacitance	C_{oss}		-	34	-	PF
Reverse Transfer Capacitance	C_{rss}		-	19.5	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=30V, I_D=1.5A$ $V_{GS}=10V, R_{GEN}=1\Omega$	-	6	-	nS
Turn-on Rise Time	t_r		-	15	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	15	-	nS
Turn-Off Fall Time	t_f		-	10	-	nS
Total Gate Charge	Q_g	$V_{DS}=30V, I_D=2.5A, V_{GS}=4.5V$	-	6	-	nC
Gate-Source Charge	Q_{gs}		-	1	-	nC
Gate-Drain Charge	Q_{gd}		-	1.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$V_{GS}=0V, I_s=2.5A$	-	-	1.2	V
Diode Forward Current (Note 2)	I_s		-	-	3	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

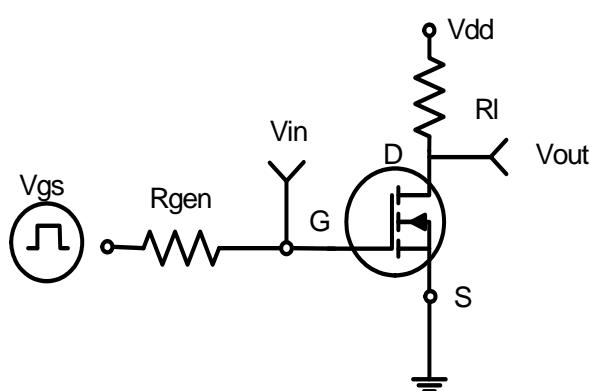


Figure 1:Switching Test Circuit

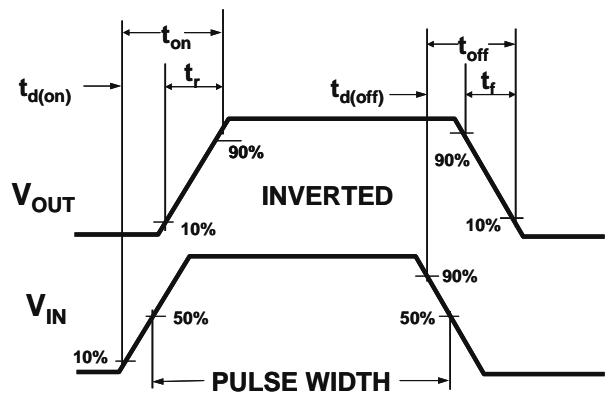


Figure 2:Switching Waveforms

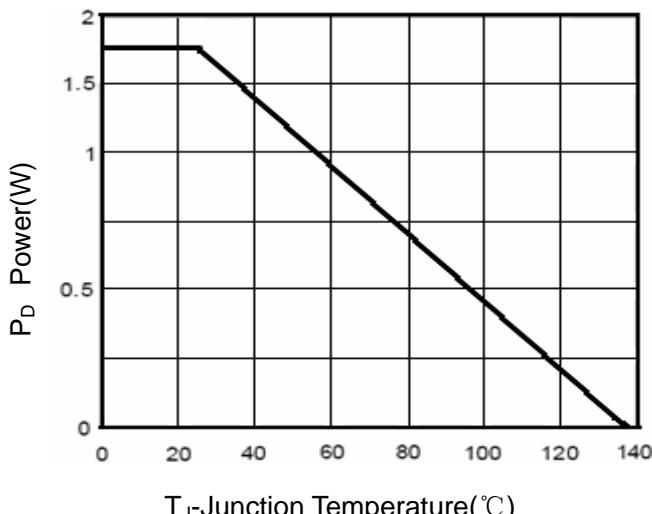


Figure 3 Power Dissipation

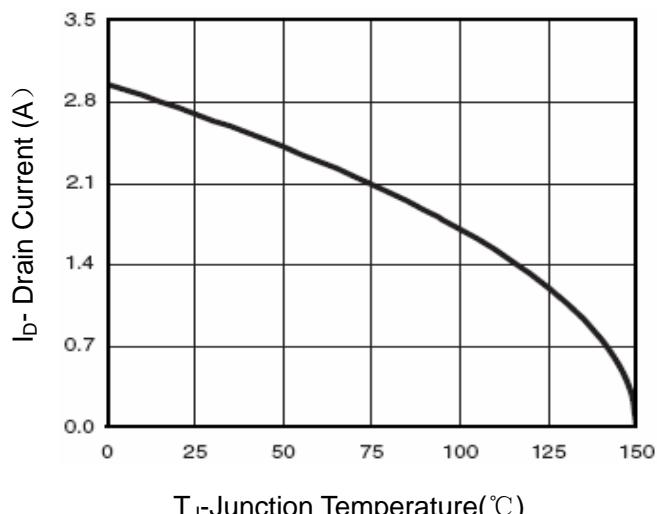


Figure 4 Drain Current

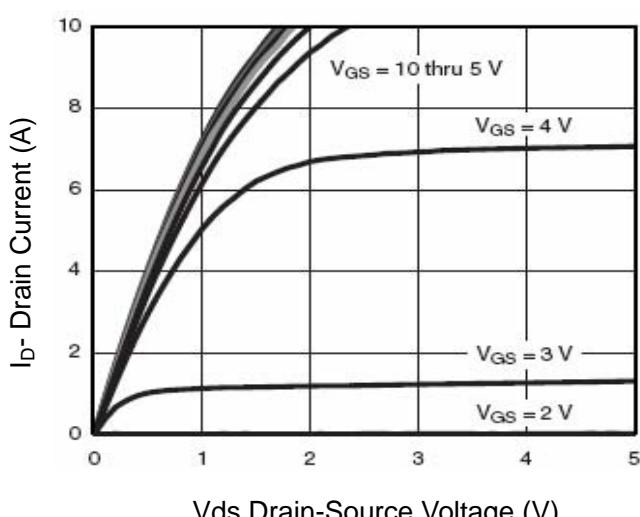


Figure 5 Output CHARACTERISTICS

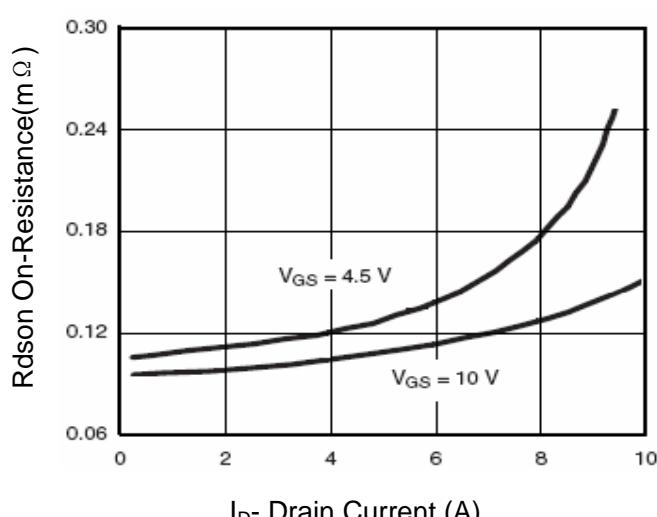
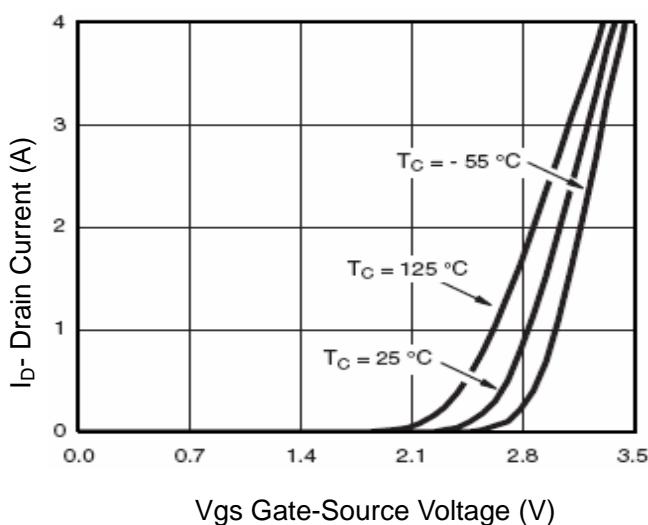
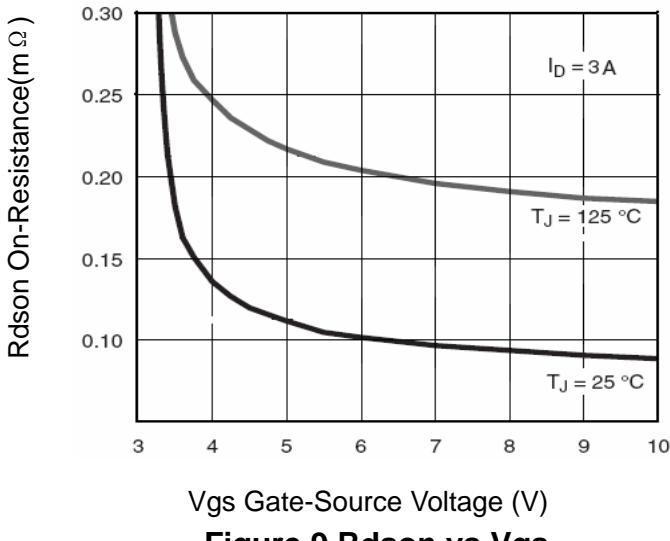
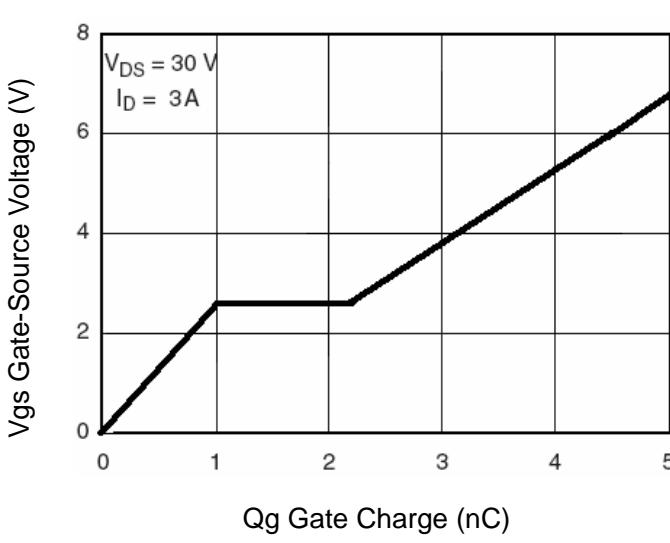
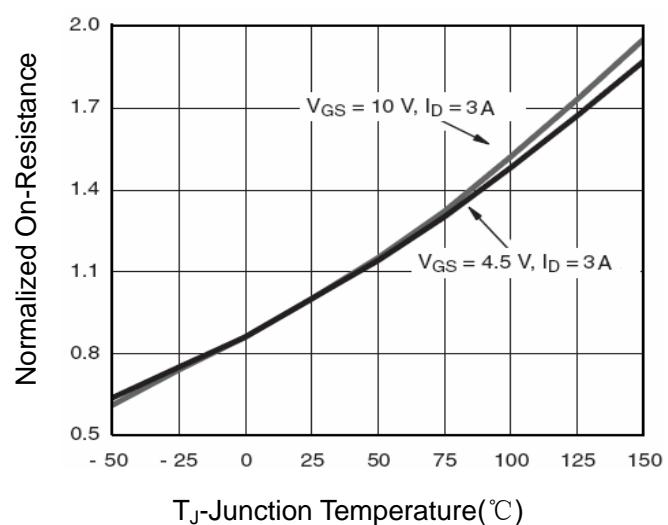
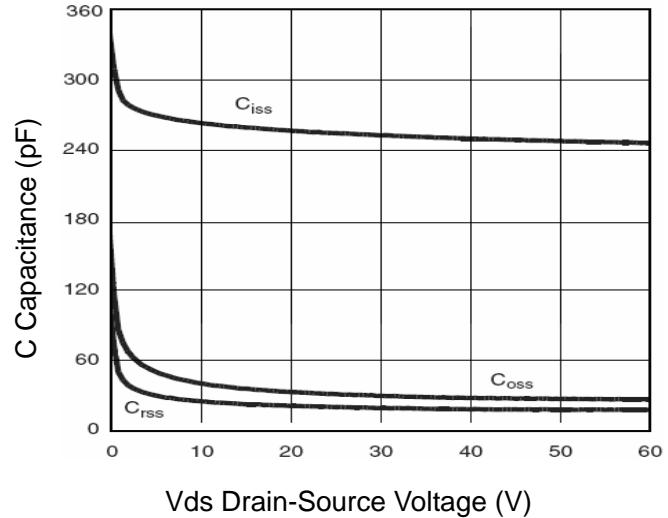
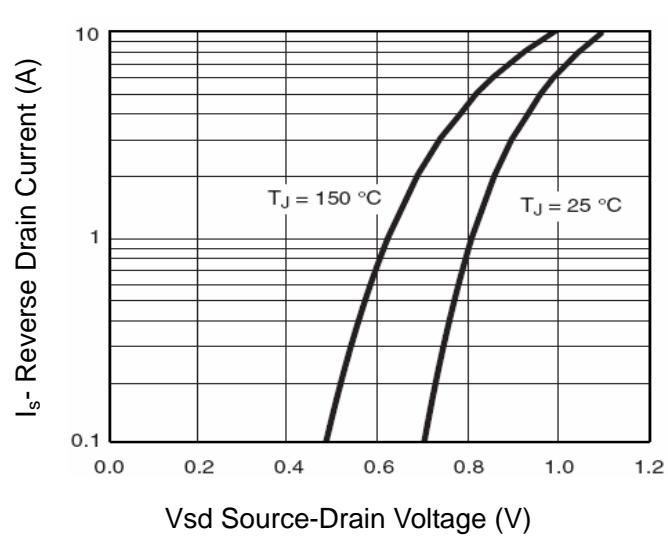
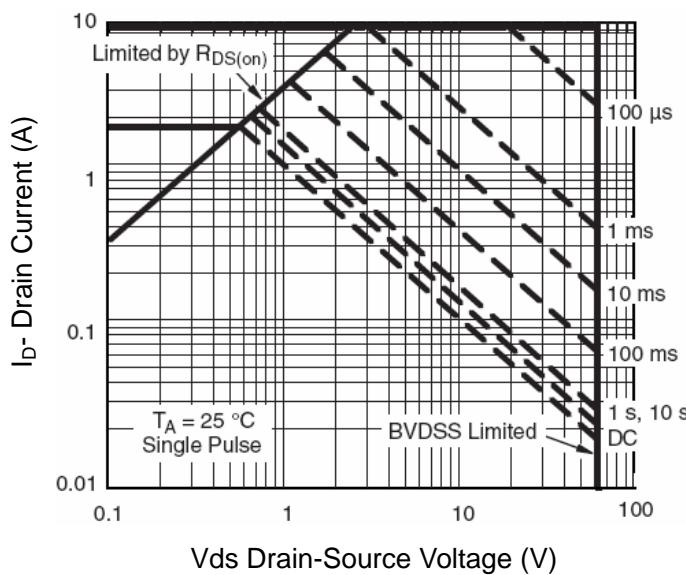
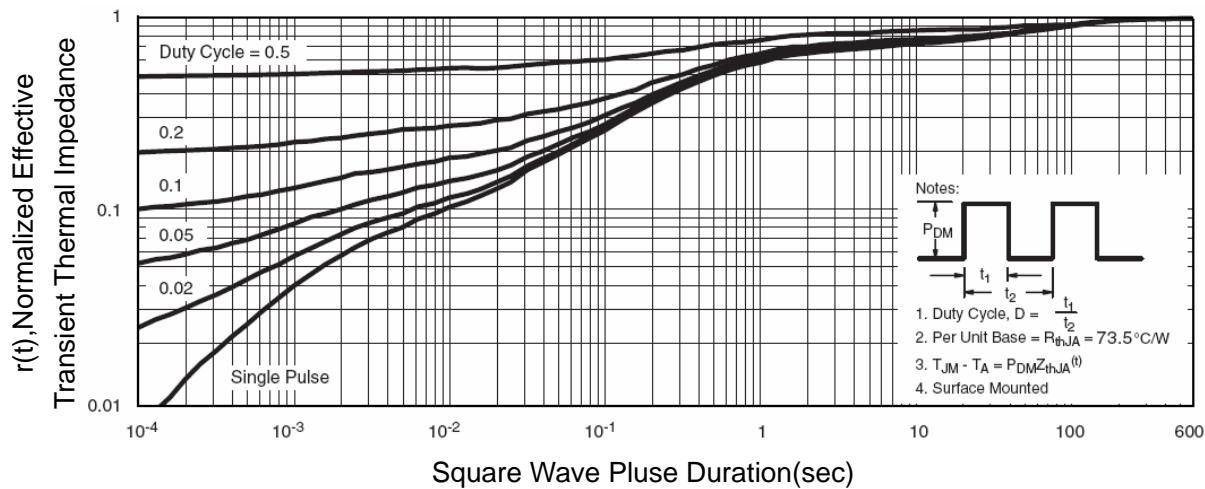
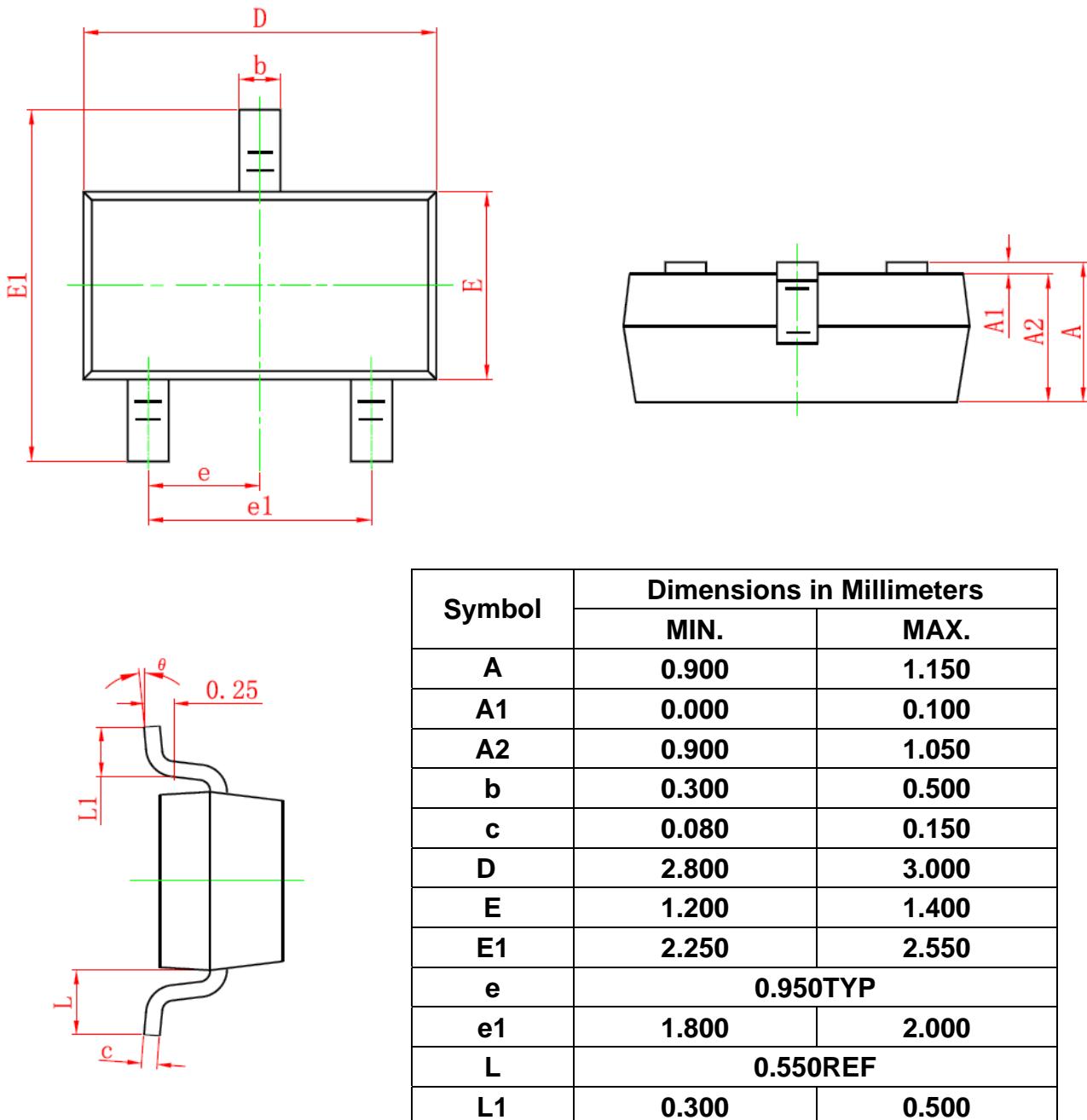


Figure 6 Drain-Source On-Resistance

**Figure 7 Transfer Characteristics****Figure 9 R_{DSON} vs V_{GS}** **Figure 11 Gate Charge****Figure 8 Drain-Source On-Resistance****Figure 10 Capacitance vs V_{DS}** **Figure 12 Source- Drain Diode Forward**

**Figure 13 Safe Operation Area****Figure 14 Normalized Maximum Transient Thermal Impedance**

SOT-23 PACKAGE INFORMATION



NOTES

1. All dimensions are in millimeters.
2. Tolerance $\pm 0.10\text{mm}$ (4 mil) unless otherwise specified
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.